

Amendments to the Claims:

The listing of claims will replace all prior version, and listing, of claims in the application.

Listing of Claims:

1.-8. (canceled)

9. (new) An arrangement in a power output stage for testing the power output stage, said power output stage having at least three half-bridges, each of said half-bridges comprising a series circuit including an upper semiconductor switch and a lower semiconductor switch connected at a junction point forming an output, said power output stage further comprising means for applying an operating voltage to each of said at least three half-bridges and windings of an at least three phase motor connected to respective outputs of said at least three half-bridges, said arrangement comprising a control device connected to each of said upper and lower semiconductor switches and running a program, said control device arranged and dimensioned for switching one or a plurality of said upper and lower semiconductor switches to an on state according to a switching state defined by the program, said control device further connected to said outputs and arranged and dimensioned for testing whether the voltages at the respective outputs lie within predetermined tolerance ranges for the switching state, said arrangement further comprising further switches for interrupting feeds to said windings.

10. The arrangement of claim 9, wherein said windings are connected by a star connection, said further switches are arranged at said star connection and at feed lines between said outputs and said windings.

11. The arrangement of claim 9, wherein said further switches comprise relays.

12. The arrangement of claim 9, further comprising voltage dividers connected to each of said outputs and said means for applying the operating voltage, wherein said control device comprises window comparators connected, via said voltage dividers, to each of said outputs and to said means for applying the operating voltage.

13. The arrangement of claim 9, further comprising a device arranged and dimensioned for causing said output voltages to lie within respective predetermined average tolerance ranges when each of said upper and lower semiconductor switches is not in the on state and when the operating voltage is applied.

14. The arrangement of claim 12, further comprising a device arranged and dimensioned for causing said output voltages to lie within respective predetermined average tolerance ranges when each of said upper and lower semiconductor switches is not in the on state, said device comprising a resistor connected between said output of one of the half bridges and said means for applying an operating voltage, said resistor being arranged and dimensioned

for generating, together with said voltage divider corresponding to said output of said one of said half bridges, a voltage in the average tolerance range.

15. The arrangement of claim 9, further comprising a controllable switch arranged between said means for applying said operating voltage and a terminal for receiving the operating voltage, and a resistor connected in parallel with said controllable switch, wherein said controllable switch is connected to said control device.